

ART 34 AMDT

CLAIMS

1. Video apparatus comprising :

- 5 - a digital encoder (4, 6) receiving a first analogue signal (A1) with ancillary information in a given time window and generating on an output a digital stream (MPEG) based at least partly on the first analogue signal (A1) ;
- a digital decoder (8) at least connectable to the output and generating a second analogue signal (A2) from the digital stream at least when
- 10 being connected to said output;
- control means (8, 14 ; 26) for determining the occurrence of said time window and correspondingly generating a control signal (CTL) ;
- selecting means (20) for selectively outputting, based on said control signal (CTL), the first analogue signal (A1), when said time window
- 15 occurs, and otherwise, the second analogue signal (A2),
- wherein the digital decoder (8) includes means for synchronising the second analogue signal (A2) to the first analogue signal (A1).

2. Video apparatus according to claim 1, wherein the means for

20 synchronising the second analogue signal (A2) to the first analogue signal (A1) are coupled to a synchronisation separator (12) receiving the first analogue signal (A1) on an input.

3. Video apparatus according to any of claims 1 to 2, wherein the

25 control means uses a first signal (VBI) which is high only during periodical predetermined time intervals corresponding to said time window.

4. Video apparatus according to claim 3, wherein the first analogue

30 signal (A1) is a CVBS signal and wherein said first signal (VBI) is high during predetermined lines of the first analogue signal (A1).

5. Video apparatus according to claim 3, wherein the control signal

35 (CTL) is generated by a combination of the first signal (VBI) and of a second periodical signal (FBL) corresponding to active parts of the first analogue signal (A1).

BEST AVAILABLE COPY

14. JUL. 2004 14:29

DTB PATENTS & ADMIN

NR. 582

S. 4

PA020012*WO*clm01*130704

2

ART 3A AMDT

6. Video apparatus according to claim 5, wherein the first analogue signal (A1) is a CVBS signal, wherein said first signal (VBI) is high during predetermined lines of the first analogue signal and wherein said second periodical signal (FBL) is high during a determined part of each line.

5

7. Video apparatus according to any of claims 1 to 6, wherein the digital encoder (4, 6) and the digital decoder (8) are coupled via a selector (7) coupled to a medium interface (10).

10

8. Video apparatus according to any of claims 1 to 7, wherein the selecting means (20) are coupled to an output (22) of the video apparatus connectable to a display.

15

9. Video apparatus comprising :

- a digital encoder (4, 6) receiving a first analogue signal (A1) and generating on an output a digital stream (MPEG) based on the first analogue signal (A1) ;

20

- a digital decoder (8) receiving the digital stream (MPEG) and generating a second analogue video signal (A2) based on the digital stream (MPEG) and synchronised with the first analogue signal (A1),

wherein the digital decoder (8) includes means for synchronising the second analogue signal (A2) to the first analogue signal (A1).

BEST AVAILABLE COPY